

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10508879
	Filing Date		2004-09-22
	First Named Inventor	Alexander Krasin	
	Art Unit	2836	
	Examiner Name	Zeev Kitov	
	Attorney Docket Number	SC11823TC	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
Z.k.	1	6684823	B2	2003-12-16	Yokoi	
	2	6903913	B2	2005-06-07	Ker et al	
	3	6198138	B1	2001-03-08	Hirota	
	4	5508649		1996-04-16	Shay	
	5	5440162		1995-08-08	Worley et al	
	6	5744842		1998-04-28	Ker	
	7	5239440		1993-08-24	Merril	
Z.k.	8	5323087		1994-06-21	Shay	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number		10508879
Filing Date		2004-09-22
First Named Inventor	Alexander Krasin	
Art Unit	2836	
Examiner Name	Zeev Kitov	
Attorney Docket Number	SC11823TC	

<i>Z-k</i>	9	5222146		1993-08-22	Bahl et al	
	10	5807464		1999-05-25	Maloney et al	
	11	6016070		2000-01-18	Uehara	
	12	5978192		1999-11-02	Young et al	
	13	6510033	B1	2003-01-21	Maloney et al	
	14	6400540	B1	2002-06-04	Chang	
	15	6392860	B1	2002-05-21	Lin et al	
	16	6385021	B1	2002-05-07	Takeda et al	
	17	6927957	B1	2005-08-09	Bakulin et al	
	18	6643109	B1	2003-11-04	Li et al	
<i>Z-k</i>	19	6795286	B2	2004-09-21	Maloney et al	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10506879	
	Filing Date		2004-09-22	
	First Named Inventor	Alexander Krasin		
	Art Unit	2836		
	Examiner Name	Zeev Kitov		
Attorney Docket Number		SC11823TC		

Z.k.	20	6566715	B1	2003-05-20	Ker et al	
------	----	---------	----	------------	-----------	--

If you wish to add additional U.S. Patent citation information please click the Add button.

U.S. PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button.

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ² j	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
Z.k.	1	02/27795	WO	A2	2002-04-04	Li et al		<input type="checkbox"/>
Z.k.	2	0851552	EP	A1	1998-07-01	Ravanelli et al		<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
Z.k.	1	RAMIREZ-ANGULO et al; "Programmable BiCMOS Transconductor for Capacitor-Transconductor Filters"; 8030 Electronic Letters, 28(1992) No. 13, Stevenage, Herts., Great Britain	<input type="checkbox"/>
Z.k.	2	MERRILL et al; "ESD Design Methodology"; ESD Symposium 93-233, pp. 5B.5.1-5B.5.5, 1993	<input type="checkbox"/>

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10508879
	Filing Date	2004-09-22
	First Named Inventor	Alexander Krasin
	Art Unit	2836
	Examiner Name	Zeev Kitov
Attorney Docket Number		SC11823TC

<i>Z.k.</i>	3	TORRES et al; "Modular, Portable, and Easily Simulated ESD Protection Networks for Advanced CMOS Technologies" <i>no date</i>	<input type="checkbox"/>
	4	DABRAL et al; "Core Clamps for Low Voltage Technologies" ESD Symposium, pp. 3.6.1-3.6.9 <i>no date</i>	<input type="checkbox"/>
	5	ANDERSON et al; "Cross-Referenced ESD Protection for Power Supplies" ESD Symposium 98-86, pp. 2A.5.1-2A.5.10, 1998	<input type="checkbox"/>
<i>Z.k.</i>	6	WORLEY et al; "Sub-Micron Chip ESD Protection Schemes Which Avoid Avalanching Junctions" ESD Symposium 95-19, pp. 1.2.1-1.2.8 <i>no date</i>	<input type="checkbox"/>

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature	<i>Z. Kitov</i>	Date Considered	<i>09/08/2006</i>
--------------------	-----------------	-----------------	-------------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.